REMARKS

Applicants respectfully request entry of the above amendments and consideration of the pending claims as amended.

Claims 1, 3-6, 9, and 11-14 as amended were rejected in a co-pending application, Serial No. 10/100,781 (the '781 Application), as being anticipated by Klim et al. To expedite prosecution in the '781 Application, Claim 1 was amended and claims 9 and 11-14 were cancelled, without prejudice. However, Applicants have elected to re-present these claims along with amended claim 7 and claim 8 in the current application. Applicants respectfully submit that pending claims 1, 3-9, and 11-14 are allowable over the references of record, including Klim for at least the reason that Klim does not disclose or suggest, among other features, a pseudo-NMOS circuit.

Claims 1, 3-9, and 11 specifically define a pseudo-NMOS circuit, and claims 12-14 specifically define a method for reducing noise at an output of a pseudo-NMOS circuit. However, Klim is not directed to a pseudo-NMOS circuit, but instead to a domino logic circuit. By definition, a pseudo-NMOS circuit has a pull-up load device that is always ON. However, Klim et al. teaches that p-channel transistor 27c is instead controlled by a clock signal. Please see Figure 2 of Klim, for example. Accordingly, Klim fails to teach or suggest a pseudo-NMOS circuit as defined in claims 1, 3-9, and 11, and also fails to teach or suggest a method for reducing noise at an output of a pseudo-NMOS circuit.

For at least these reasons, Applicants request consideration and allowance of the pending claims. The Examiner is respectfully requested to call Applicants' attorney if an interview would expedite prosecution.

Respectfully submitted,
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